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POWER OF ATTORNEY TO PROSECUTE APPLICATIONS BEFORE THE USPTO

I hereby revoke all previous powers of attorney given in the application identified in the attached statement under 37 CFR 3.73(b). I hereby appoint: 27765 Practitioners associated with the Customer Number: OR Practitioner(s) named below (if more than ten patent practitioners are to be named, then a customer number must be used): Registration Registration Name Name Number Number as attorney(s) or agent(s) to represent the undersigned before the United States Patent and Trademark Office (USPTO) in connection with any and all patent applications assigned only to the undersigned according to the USPTO assignment records or assignment documents attached to this form in accordance with 37 CFR 3.73(b). Please change the correspondence address for the application identified in the attached statement under 37 CFR 3.73(b) to: 27765 The address associated with Customer Number: ORFirm or Individual Name Address Zip State City Country Email Telephone Assignee Name and Address: LITE-ON TECHNOLOGY CORP. 22Fl. 392, Ruey Kuang Road, Neihu, Taipei, Taiwan, R.O.C. A copy of this form, together with a statement under 37 CFR 3.73(b) (Form PTO/SB/96 or equivalent) is required to be filed in each application in which this form is used. The statement under 37 CFR 3.73(b) may be completed by one of

Title AVP

This collection of information is required by 37 CFR 1.31, 1.32 and 1.33. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

the practitioners appointed in this form if the appointed practitioner is authorized to act on behalf of the assignee,

SIGNATURE of Assignee of Record

The individual whose signature and title is supplied below is authorized to act on behalf of the assignee

Date

Telephone +886-2-87982888

and must identify the application in which this Power of Attorney is to be filed.

Signature

Name

Yih-Lin Lu

Approved for use through 10/31/2007. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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STATEMENT UNDER 37 CFR 3.73(b)	
Applicant/Patent Owner: LITE-ON TECHNOLOGY CORP.	
Application No./Patent No.: US 6,717,820 B1 Filed/Issue Date: 04/06/2004	
Entitled: Circuit Board Assembly	
LITE-ON TECHNOLOGY CORP. , a corporation (Name of Assignee) , a corporation (Type of Assignee, e.g., corporation, partnership,	university, government agency, etc.)
states that it is: 1. the assignee of the entire right, title, and interest; or	
an assignee of less than the entire right, title and interest (The extent (by percentage) of its ownership interest is%)	
in the patent application/patent identified above by virtue of either:	
A. An assignment from the inventor(s) of the patent application/patent identified above. The a in the United States Patent and Trademark Office at Reel, Frame thereof is attached. OR B. A chain of title from the inventor(s), of the patent application/patent identified above, to the	, or for which a copy
From: KAH PHANG LOH To: AGILENT TECHNOLOGIES The document was recorded in the United States Patent and Trademark Office at Reel 010876 , Frame 0268 , or for which a copy thereof is at 2. From: AGILENT TECHNOLOGIES, INC. To: AVAGO TECHNOLOGIES GENER	
The document was recorded in the United States Patent and Trademark Office at Reel 017207 , Frame 0020 , or for which a copy thereof is	
3. From: AVAGO TECHNOLOGIES GENERAL IP (SINGAPORE) PTE. LTD. To: CITICORP NORTH AMERICA, INC. The document was recorded in the United States Patent and Trademark Office at Reel 017207 , Frame 0882 , or for which a copy thereof i	s attached.
Additional documents in the chain of title are listed on a supplemental sheet.	
As required by 37 CFR 3.73(b)(1)(i), the documentary evidence of the chain of title from the assignee was, or concurrently is being, submitted for recordation pursuant to 37 CFR 3.11.	e original owner to the
[NOTE: A separate copy (i.e., a true copy of the original assignment document(s)) must be a Division in accordance with 37 CFR Part 3, to record the assignment in the records of the 302.08]	
The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.	OCT 2 9 2008
Signature	Date
Yih-Lin Lu	+886-2-87982888
Printed or Typed Name	Telephone Number
AVP	
Title	

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Supplemental Sheet

4. From: AVAGO TECHNOLOGIES GENER	RAL IP (SINGAPORE) PTE_LTD_10	O: AVAGO TECHNOLOGIES ECBU IP (SINGAPORE) PIE LID
The document was recorded	d in the United States Pat	ent and Trademark Office at
Reel017675	Frame <u>0518</u> , or	for which a copy thereof is attached.
5. From: AVAGO TECHNOLOGIES ECBU I	IP (SINGAPORE) PTE. LTD. To	: LITE-ON TECHNOLOGY CORP.
The document was recorded	d in the United States Pat	ent and Trademark Office at
Reel,	Frame, or	for which a copy thereof is attached.

PATENT ASSIGNMENT

This Patent Assignment (this "Assignment") is made as of 25 Januray 2008 by and between Avago Technologies General IP (Singapore) Pte. Ltd., (Company Registration No. 200512430D), Avago Technologies ECBU IP (Singapore) Pte. Ltd., (Company Registration No. 200512325K), Avago Technologies Fiber IP (Singapore) Pte. Ltd., (Company Registration No.) each having a principal place of business at No.1 Yishun Avenue 7, Singapore 768923, both companies incorporated under the laws of Singapore (collectively "Assignor"), and Lite-On Technology Corporation, and its Affiliates, a Taiwan corporation, having a principal place of business at 12F, 392, Ruey Kuang Road, Neihu, Taipei 114, Taiwan, R.O.C. (collectively "Assignee").

RECITALS

WHEREAS, the parties have entered into an Asset Purchase Agreement, dated as of October 2007 ("Asset Purchase Agreement") whereby in furtherance thereof the parties desire to exchange certain patents and patent applications, and Assignor now desires to assign to Assignee all of Assignor's right, title and interest in and to the patents and/or patent applications set forth in Exhibit A to this Assignment ("the Patents").

ASSIGNMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Purchase Agreement and the covenants and agreements in this Assignment, Assignor agrees as follows:

- 1. Assignor does, subject to all existing license grants, hereby transfer, convey, assign and deliver to Assignee all of Assignor's right, title and interest in and to the Patents, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made, and all claims for damages and other remedies for past and future infringements of the Patents, along with the right to sue for and collect such damages and other remedies for the use and benefit of Assignee and its successors, assigns and other legal representatives.
- 2. Assignor agrees to execute and deliver such other documents and to take all such other actions which Assignee, its successors and/or assigns may reasonably request to effect the terms of this Assignment, and to execute and deliver any and all affidavits, testimonies, declarations, oaths, samples, exhibits, specimens and other documentation as may be reasonably required to effect the terms of this Assignment and its recordation in relevant state, national or international patent offices.

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

ASSIGNOR

AVAGO T	TECHNOLOGIES GENERAL IP (SINGAPORE) PTE. LTD.
By: Name: Title:	Hanry Johnson
Assigno	R
AVAGO	TECHNOLOGIES ECBU IP (SINGAPORE) PTE. LTD.
By: Name: Title:	J/man
ASSIGNO	\mathbf{R}
Avago 7	TECHNOLOGIES FIBER IP (SINGAPORE) PTE. LTD.
Ву:	- V/m-7/A
Name: Title:	The state of the s

Exhibit A

Schedule 1 - Patents

Case Number			Comment	
1094966	US5600128	Limiting Front End For Infrared Data Communications For Wide Dynamic Range & Low Latency		
70004160	US7218860	Transceiver Module		
70010851	US6762472	Signal Communication Structures And Methods For Making The Same		
70010962	US20040213576	Optical Transceiver For Data Transfer And Control Applications		
70021062	US20040165277	Single Lens Multiple Light Source Device		
70030253	US20060045530	Compact Optical Transceiver Module		
70040904	US20060198032	Bi-Curvature Lens For Light Emitting Diodes And Photo Detectors		
70040948	US20060081779	Variable Noise Control For An Optical Transducer		
70041073	US7220953	Method And Apparatus To Speed Up Response Time Of Photodiode By Limiting Maximum P-I-N Capacitance Via Voltage Clamping		
70041074	US20060216042	Automatic Gain Control Circuit For Infrared Receiver		
70041510	US20060291114	Method And Apparatus To Overcome Stuck-High And Over Sensitivity In Na Ambient Photo-Current Sensing For Wide Dynamic Range Sensing (na To Ma) Application		
70050337	US20070088790	Method Of Matching Capacitance Of PIN Diode To Achieve Better Common Mode Rejection Ratio		
70050341	US20070018685	Multi-Stage Light Emitting Diode Driver Circuit		
70050604	US20070003289	Integrated Transceiver For Remote Interactive Applications		
70050920	US7265626	Fast-Settling Digital Automatic Gain Control		
70050921	Unpublished	Method And Apparatus For Demodulating Amplitude Shift Key (ASK) Modulated Wave With Better Noise Performance		
70990046	US6717820	A Packaging Method Which Reduces The Effective Height (measured From PCB Surface) Of A Product By Allowing The Package To Be Placed Inside An Open Slot Of Main PCB		
10982407	GB2360656	Low Cost, High Speed, High Efficiency Infrared Transceiver	Abandoned	

Case Number	Patent/IP	Туре	Description	Comment
10051295	Unpublished	Patent	IrDA Optical Links For Computer Mouse Or Keyboard	***************************************
10961102	US6330092	Patent	Polarization Based Differential Receiver For Reduction Of Background In Free-Space Optical Links	
10971896	US6738583	Patent	A Micro-machined Active IR Link With Significantly Wider Coverage	

10982407	GB2360656	Patent	Low Cost High Speed, High Efficiency Infrared Transceiver		
40030912	US20050029527	Patent	LED Drive Circuit	(CANCILLE STATE OF THE STATE OF	
70010821	US6906325	Patent	A Photo-Receiver Arrangement		* :.
70041121	US20060067707	Patent	System And Method For Increasing Data Communication Bandwidth In A Light Communication System		·
70051702	Unpublished	Patent	Concept Of Using Cross Wire Bonding To Enhance EMC Immunity Within A Molded Package		
70060021	Unpublished	Patent	Concept Of Grounding External EMI Shield Without Having Extra Solder Pad On Main PCB	anakan kan kan kan kan kan kan kan kan k	i de la composition della comp
70060023	Hold	Patent	Concept Of Effective Heat Dissipation By Introducing Via Hole Under The Die Within A Reflector Cup In PCB Substrate		
70060051	Hold	Patent	Low Cost Thermal Cooling For IrDA Multiple-Chip- Module Using Ring Spreader & Plated-Through-Via		
IrDA SW	IrDA Software Stack	IP	IrDA Software Protocol Stack		· · · · · · · · · · · · · · · · · · ·
IrDA SW	IrDA Test Suite	IP .	IrDA Software Protocol Test Suite		
IrDA SW	IrDA RC application	P	IrDA RC application Software & GUI		
WH Adri	IrDA VFIR IP Core	TP T	IrDA VFIR RTL IP Core (Soft Core)		